

Attorney's Docket No. 004860.P3281

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of:

Thomas J. Wilson

Examiner: Not Yet Assigned

Application No.: 10/753,032

Art Unit: Not Yet Assigned

Filed: January 6, 2004

For: Method and Apparatus for the
Generation and Control of Clock
Signals

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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X 37 C.F.R. §1.97(b).

_____ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

_____ A statement pursuant to 37 C.F.R. §1.97(e) or

_____ A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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(1) A statement pursuant to 37 C.F.R. §1.97(e); and

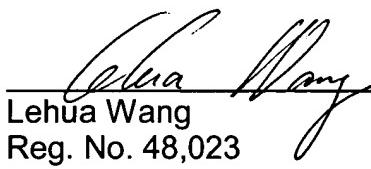
(2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

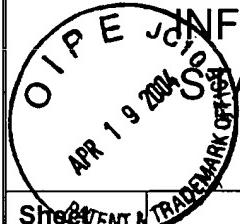
Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/15, 2004


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Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Sheets 1000 TR

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Sheet 1 of 1 ATTORNEY DOCKET NUMBER 004860.P3281

<i>Complete if Known</i>	
Number	10/753,032
	January 6, 2014
Inventor:	Thomas J. Wilson
	Not Yet Assigned
Name	Not Yet Assigned

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Sheet 2 of 2 Attorney Docket Number 004860.P3281

Complete if Known

Application Number	10/753,032
Filing Date	January 6, 2004
First Named Inventor:	Thomas J. Wilson
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		APPEL, Gary, "Fractional N synthesizers," RF signal processing, November 2000, pp. 34-50.	
		BARRETT, Curtis (ed.), "Fractional/Integer-N PLL Basics," Texas Instruments Technical Brief SWRA029, August 1999, 55 pgs.	
		"Apple Unveils World's First 17-inch Notebook," January 7, 2003.	
		Cypress Press Release, "Cypress Announces Clock Support for Intel's Next-Generation Pentium® 4 Processor," September 8, 2003.	

Examiner Signature	Date Considered
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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